Substitute for form 1448A/PTO

Sheet

## **INFORMATION DISCLOSURE** STATEMENT BY APPLICANT

of

Complete if Known					
Application Number	10/812,242				
Filing Date	March 29, 2004				
First Named Inventor	Chad Thomas Steward				
Art Unit					
Examiner Name					
Attorney Docket Number	\$\$122B208				

NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-tissue number(s), publisher, dily ancilor country where published. Examiner Initials\* Cite No.' GREBENE, ALAN B., "Bipolar and MOS Analog Integrated Circuit Design", 1984, No month Micro-Linear Corporation, Sunnyvale, California, Pgs. 853-855

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Examiner	11	1 111			Date	1/	///		
Signature		LV	n_	_ /	Considered	///	16/	04	
Examiner: Initia	reterance co	nsideral, who	ther or not distion	to Contomistics with MP	EP 800. Draw the throug	n chaigh if n	ol in cograme	nce and not	considered, Include

n number. "Applicant is to place a check mark here if English language Translation is situated.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Teylor & Zeitmen (wir) 08/11/2003. Send To: Commissioner for Palents, P.O. Box 1450, Alexandria, VA 22313-1450